

## DECLARATION FOR PATENT APPLICATION

UNITED STATES

Docket No. NECR 20.838

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: SEMICONDUCTOR DEVICE INCLUDING P-CHANNEL TYPE TRANSISTOR, AND PRODUCTION METHOD FOR MANUFACTURING SUCH SEMICONDUCTOR DEVICE

the specification of which

(check one)

☒ is attached hereto☐ was filed on \_\_\_\_\_ as

Application Serial No. \_\_\_\_\_

and was amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by an amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations §1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

2003-022764

(Number)

Japan

(Country)

30/01/2003

(Day/Month/Year Filed)

Priority Claimed

☒ Yes

No

2003-416411

(Number)

Japan

(Country)

15/12/2003

(Day/Month/Year Filed)

☒ Yes

No

(Number)

(Country)

(Day/Month/Year Filed)

Yes

No

I hereby claim the benefit under Title 35, United States Code §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations §1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)

(Filing Date)

(Status-patented, pending, abandoned)

(Application Serial No.)

(Filing Date)

(Status-patented, pending, abandoned)

I hereby appoint as my attorney and agent Aaron B. Karas, Reg. No. 18,923, Samson Helfgott, Reg. No. 23,072, Linda S. Chan, Reg. No. 42,400, Michael Markowitz, Reg. No. 30,659, Brian Myers, Reg. No. 49,947, Harris Wolin, Reg. No. 39,432, Shahan Islam, Reg. No. 32,507, Emma Shleifer, Reg. No. 29,734, Serle Mosoff, Reg. No. 25,900 and Thomas J. Bean, Reg. No. 44,528 to prosecute this application and to transmit all business in the Patent and Trademark Office connected therewith.

Address all correspondence to:

Katten Muchin Zavis Rosenman  
575 Madison Avenue  
New York, New York 10022-2585  
Telephone No.: (212) 940-8800

Docket No.: \_\_\_\_\_

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole or first inventor ICHIRO YAMAMOTO  
Inventor's Signature Ichiro Yamamoto (山本) Date December 12, 2003  
Residence Kanagawa, Japan Citizenship Japanese  
Post Office Address c/o NEC Electronics Corporation, 1753 Shimonumabe,  
Nakahara-ku, Kawasaki-shi, Kanagawa, Japan

Full name of second joint inventor, if any NAOHIKO KIMIZUKA  
Second Inventor's Signature Naohiko Kimizuka (木村) Date December 12, 2003  
Residence Kanagawa, Japan Citizenship Japanese  
Post Office Address c/o NEC Electronics Corporation, 1753 Shimonumabe,  
Nakahara-ku, Kawasaki-shi, Kanagawa, Japan

Full name of third joint inventor, if any \_\_\_\_\_  
Third Inventor's Signature \_\_\_\_\_ Date \_\_\_\_\_  
Residence \_\_\_\_\_ Citizenship \_\_\_\_\_  
Post Office Address \_\_\_\_\_

Full name of fourth joint inventor, if any \_\_\_\_\_  
Fourth Inventor's Signature \_\_\_\_\_ Date \_\_\_\_\_  
Residence \_\_\_\_\_ Citizenship \_\_\_\_\_  
Post Office Address \_\_\_\_\_

Full name of fifth joint inventor, if any \_\_\_\_\_  
Fifth Inventor's Signature \_\_\_\_\_ Date \_\_\_\_\_  
Residence \_\_\_\_\_ Citizenship \_\_\_\_\_  
Post Office Address \_\_\_\_\_